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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/083,611	02/27/2002	Yoshiaki Hisamune	8020-1002-1	3150

466 7590 05/08/2003  
YOUNG & THOMPSON  
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ARLINGTON, VA 22202

EXAMINER
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BOOTH, RICHARD A

ART UNIT	PAPER NUMBER
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2812

DATE MAILED: 05/08/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

10/083,611

HISAMUNE ET AL.

Examiner

Art Unit

Richard A. Booth

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*-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --***Period for Reply****A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.**

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**1) Responsive to communication(s) filed on 28 February 2003.2a) This action is FINAL.      2b) This action is non-final.3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.**Disposition of Claims**4) Claim(s) 1-10 is/are pending in the application.

4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.

5) Claim(s) \_\_\_\_\_ is/are allowed.6) Claim(s) 1-10 is/are rejected.7) Claim(s) \_\_\_\_\_ is/are objected to.8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.**Application Papers**9) The specification is objected to by the Examiner.10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

11) The proposed drawing correction filed on \_\_\_\_\_ is: a) approved b) disapproved by the Examiner.

If approved, corrected drawings are required in reply to this Office action.

12) The oath or declaration is objected to by the Examiner.**Priority under 35 U.S.C. §§ 119 and 120**13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).a) All b) Some \* c) None of:1. Certified copies of the priority documents have been received.2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).a) The translation of the foreign language provisional application has been received.15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.**Attachment(s)**1) Notice of References Cited (PTO-892)4) Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_.2) Notice of Draftsperson's Patent Drawing Review (PTO-948)5) Notice of Informal Patent Application (PTO-152)3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) \_\_\_\_\_.6) Other: \_\_\_\_\_

**DETAILED ACTION**

***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1-7 and 10 are rejected under 35 U.S.C. 102(e) as being anticipated by Bergemont, U.S. Patent 5,847,426.

Bergemont shows the invention as claimed including forming a plurality of integrated nonvolatile memory cells comprising a lower floating gate of polysilicon 114; a control gate 102 formed on said lower gate 114 through an insulation film 116; a diffusion layer (110,112) disposed through implantation adjacent to each of opposite end portions of said lower floating gate, wherein a device isolation shielding electrode formed of the same polysilicon material as the floating gate is formed outside said diffusion layer disposed adjacent to said opposite end portions of said lower floating gate; and said device isolation shielding electrode 104 extends in parallel with said lower floating gate 114 between plural of said nonvolatile memory cells in a completed device and is patterned from the same polysilicon layer (see Figure 9) to cover adjacent ones of said memory cells (see column 4, line 62 – column 5, line 55), wherein said diffusion layer (110,112) forms a diffusion line extending, in the completed device,

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between adjacent ones of said nonvolatile memory cells. Additionally, a floating gate covering material 209 is formed as well as a second conductive layer 210 extending perpendicular to the floating gate (see fig. 17A).

***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 8-9 are rejected under 35 U.S.C. 103(a) as being unpatentable over Bergemont, U.S. Patent 5,847,426 in view of Saitoh, U.S. Patent 5,895,720.

Bergemont is applied as above but fails to expressly disclose forming an upper floating gate with a larger cross-sectional area than the lower floating gate. Saitoh discloses forming a lower layer floating gate 24b2 and an upper layer floating gate 24c2 (see Figure 7E). In view of this disclosure, it would have been obvious to one of ordinary skill in the art at the time the invention was made to use the two layer gate of Saitoh in the Bergemont reference because the lower layer prevents fluctuations in device characteristics while the upper layer enhances etch rates for forming the floating gates in the form of a matrix (see abstract).

***Response to Arguments***

Applicant's arguments filed 2-28-03 have been fully considered but they are not persuasive. Applicant argues that the Bergemont reference fails to teach the newly added limitations. However, as broadly interpreted, the limitations are shown in figures 9-11. For example, note that the heavily doped regions 110 are all of the same width and extend between adjacent memory cells and note that the shielding electrode 104 extends between two adjacent memory cells. Note that it is clear from the reference that the shielding electrode does not extend continuously as does the diffusion line (see fig. 10). However, this feature has not been claimed.

***Conclusion***

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of

the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Richard A. Booth whose telephone number is 308-3446. The examiner can normally be reached on Monday-Thursday from 7:30-6:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Niebling can be reached on 308-3325. The fax phone numbers for the organization where this application or proceeding is assigned are 308-7724 for regular communications and 308-7724 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 308-1782.



Richard A. Booth  
Primary Examiner  
Art Unit 2812

May 6, 2003